



Superjunction MOSFET with a Trench Contact and Embedded SiO2 Insulator for Excellent Reverse Recovery

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ABSTRACT

A new superjunction MOSFET (SJ-MOSFET) with a trench contact and embedded SiO_2 insulator (ESO) is proposed and investigated by TCAD simulations. At the reverse conduction state, since electrons in the n-pillar can be easily collected by the trench contact, the hole injection efficiency of the body diode can be lowered to reduce the reverse recovery charge (Q_{rr}) . In addition, the SiO_2 insulator is embedded in the p-pillar, which can increase the resistance of the hole extraction path during reverse recovery so as to reduce the current recovery rate (d_{ir}/dt) from the peak reverse current (I_{rrm}) to zero. Simulation results show that, compared with the conventional SJ-MOSFET, Q_{rr} and d_{ir}/dt of the proposed SJ-MOSFET can be reduced by 44% and 82%, respectively.

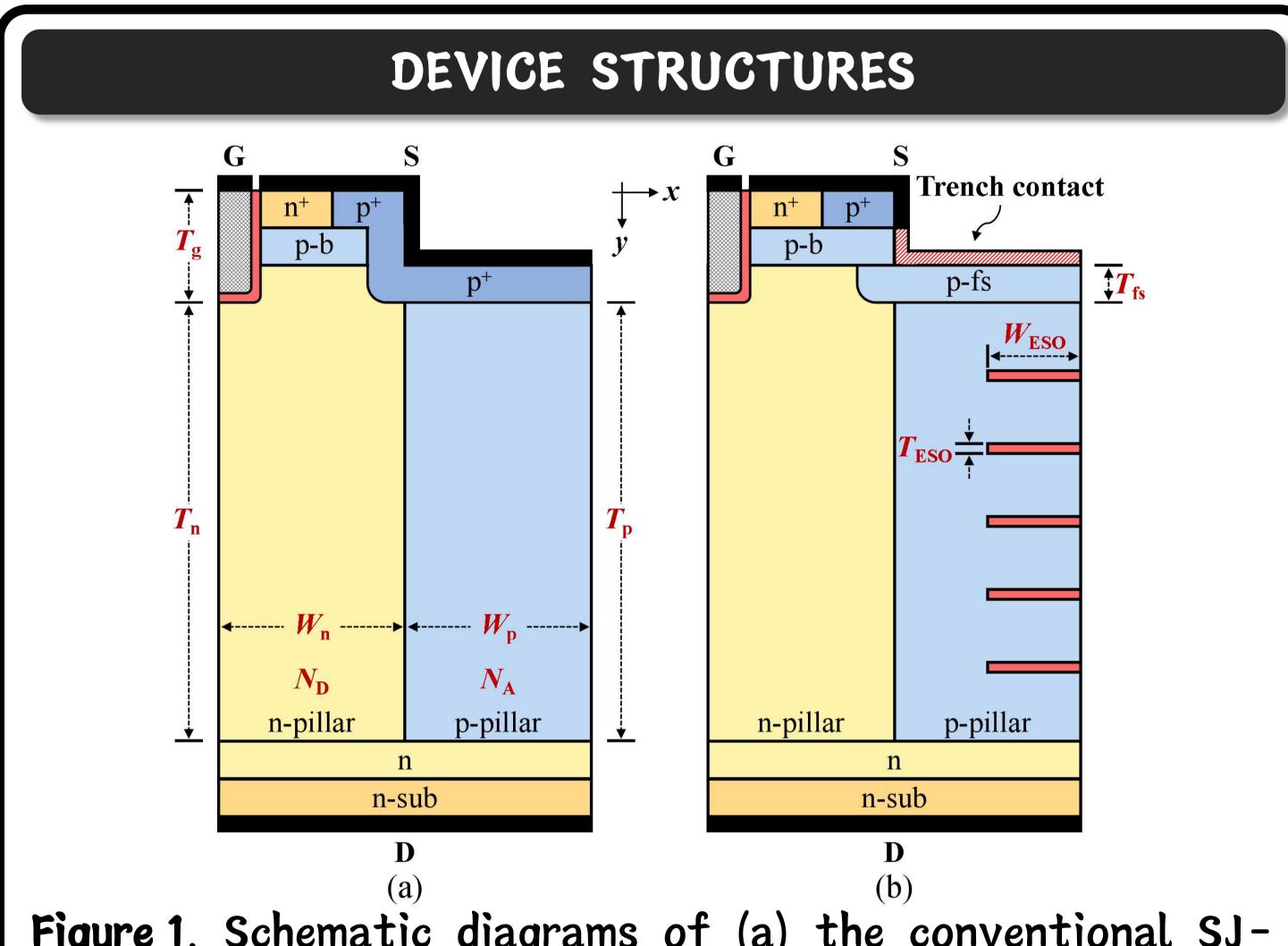


Figure 1. Schematic diagrams of (a) the conventional SJ-MOSFET; (b) the proposed SJ-MOSFET.

The trench contact on the p-b region and p-fs layer can be a Schottky contact or ohmic contact. Furthermore, a 5-layer SiO₂ insulator is embedded in the p-pillar.

KEY PARAMATERS Prop. SJ Parameters Conv. SJ $N_{\rm D}$ (cm⁻³) 4×10^{15} 4×10^{15} N_A (cm⁻³) 4×10¹⁵ 4×10¹⁵ W_n (µm) W_{p} (µm) T_n (µm) 38 T_{p} (µm) 36 T_{q} (µm) 2.5 T_{fs} (µm) 0.5 $D_{\rm fs}$ (cm⁻²) 1.7×10^{12} $W_{\rm ESO}$ (µm) 1.0 $T_{\rm ESO}$ (µm) 0.1

Table 1. Key Parameters of the conventional SJ-MOSFET and the proposed SJ-MOSFET.

The number of Embedded SiO₂ Insulator (ESO) layers are 5. The doping dose of the p-fs layer ($D_{\rm fs}$) is set to be 1.7 $\times 10^{12}$ cm⁻² to avoid the punch-through breakdown.

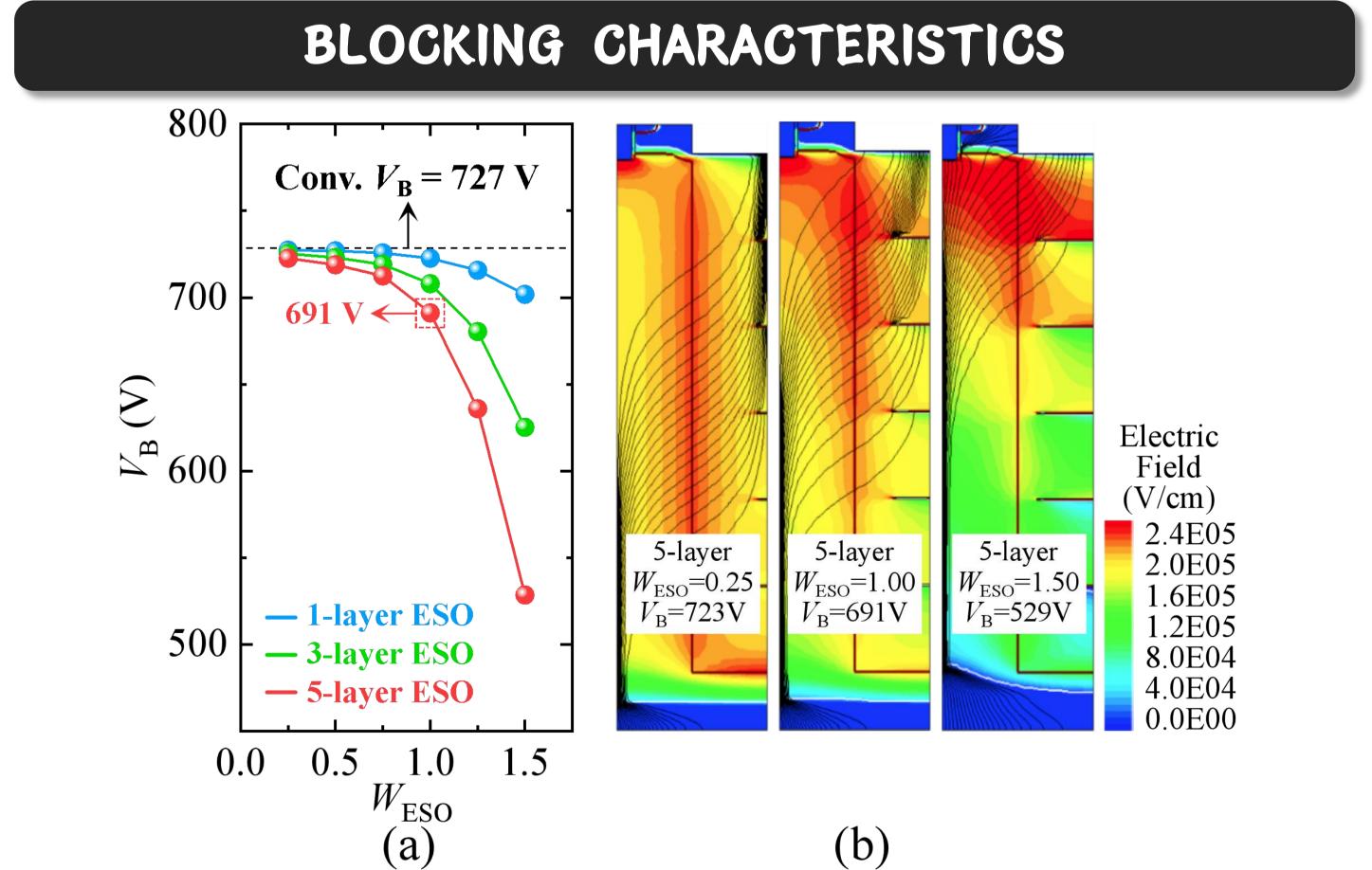


Figure 2. (a). Effects of $W_{\rm ESO}$ on breakdown voltage; (b) Distributions of electric field and current flowlines under $V_{\rm B}$.

With the increase of $W_{\rm ESO}$ and the number of ESO layers, $V_{\rm B}$ shows a decreasing trend. $V_{\rm B}$ of the Conv. and the Prop. are 727 V and 671 V, respectively.

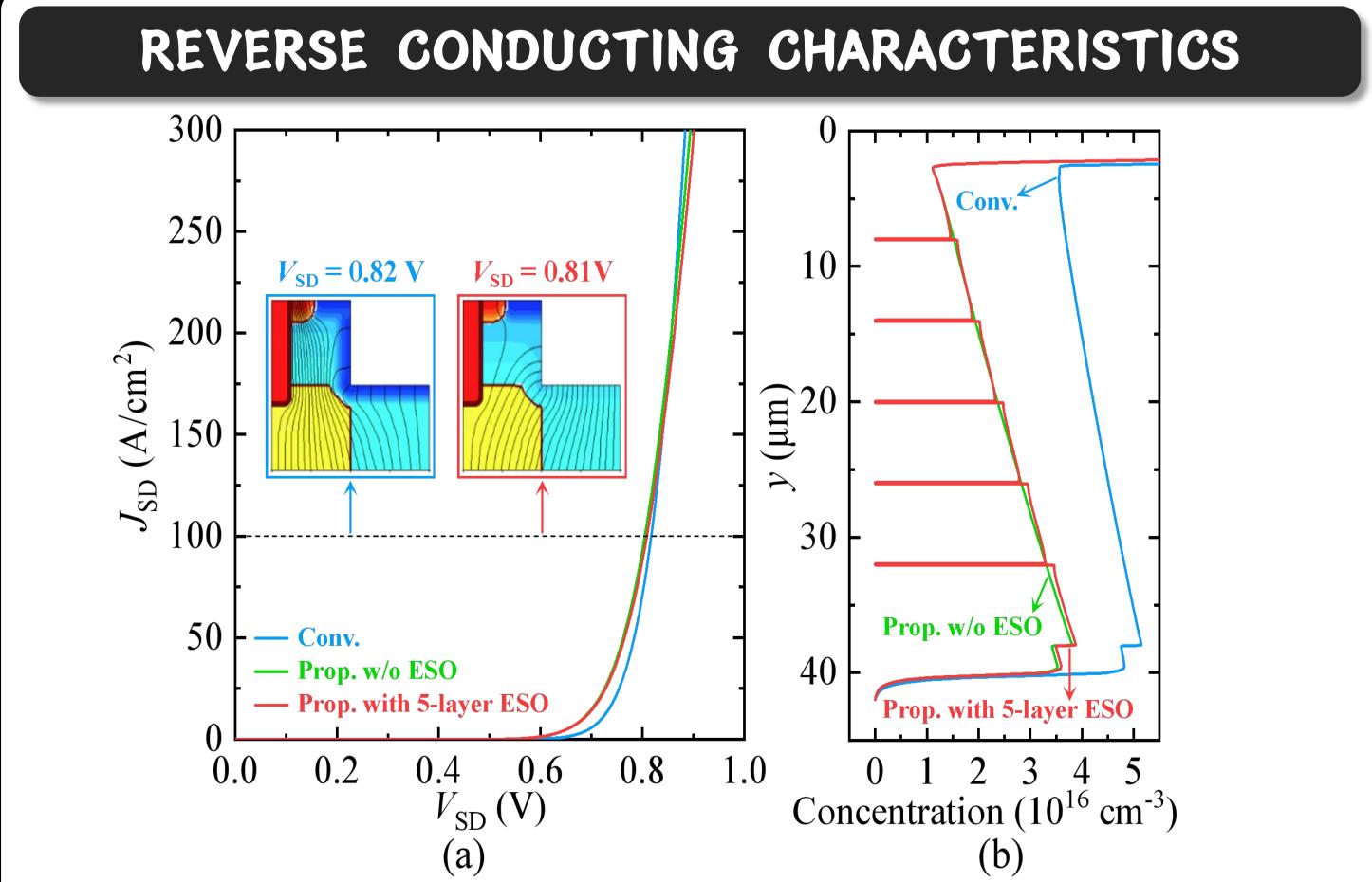


Figure 3. (a). Reverse conducting /Vs and current flowlines distributions at $J_{SD} = 100 \text{ A/cm}^2$; (b). Hole distributions along x = 4 µm at $J_{SD} = 100 \text{ A/cm}^2$.

The carrier-storage in the p-pillar of the proposed SJ-MOSFET and the case without the ESO is much lower than that of the conventional SJ-MOSFET.

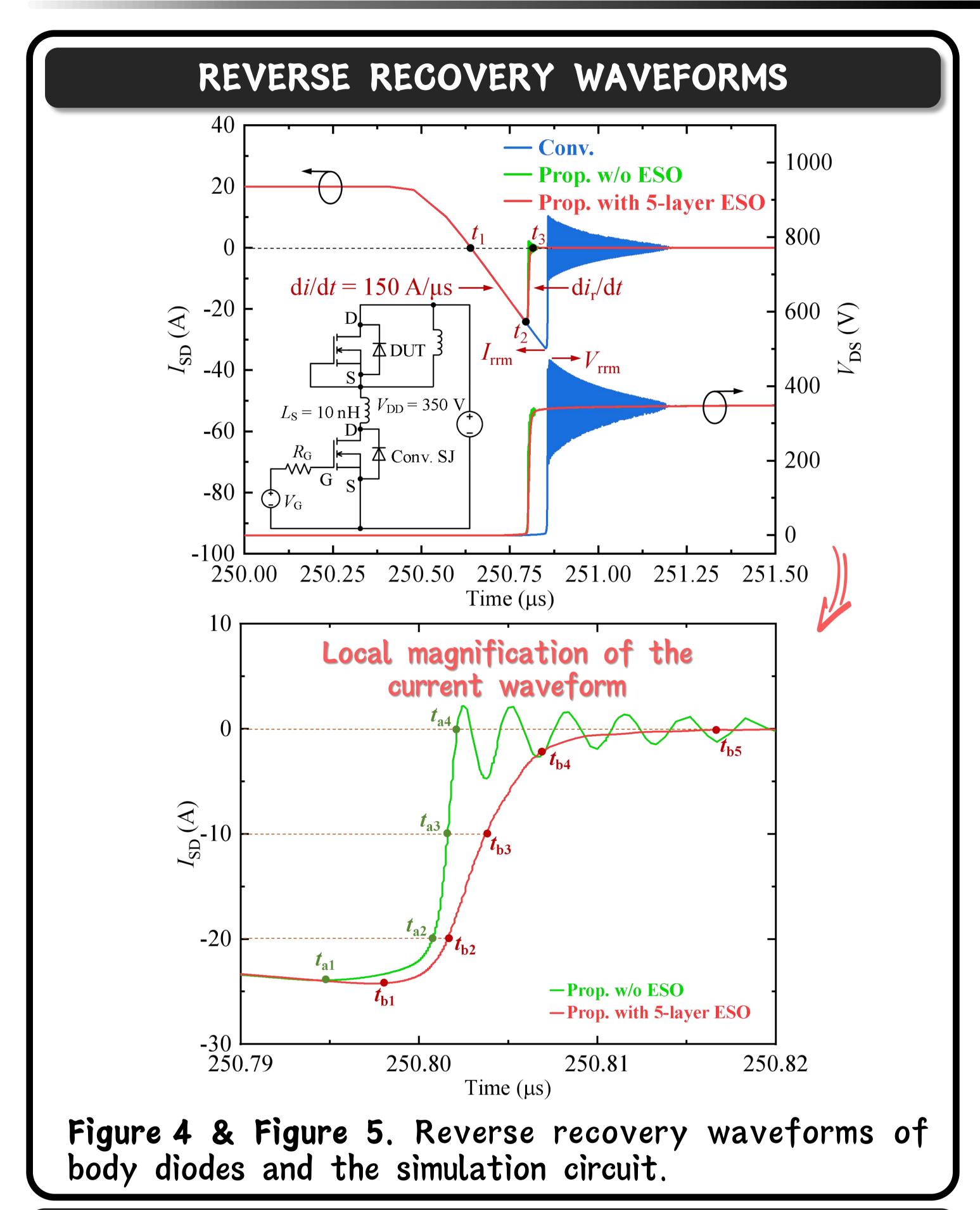




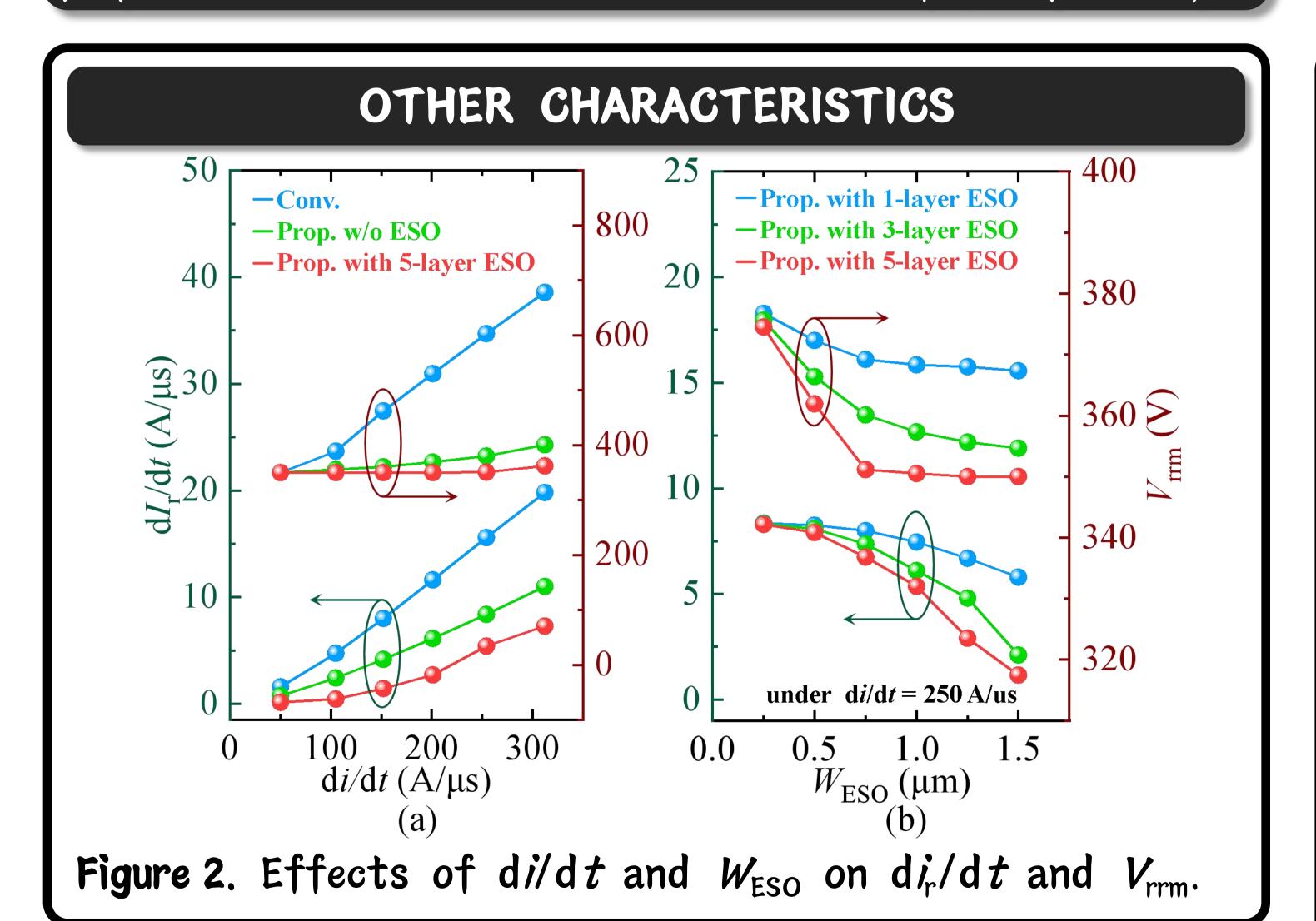
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The t_r and the di_r/dt of the conventional SJ-MOSFET are 3.93 ns and 8004 A/ μ s, respectively. The t_r and the di_r/dt of the proposed SJ-MOSFET are 16.17 ns and 1432 A/ μ s, respectively.



With the increase of $W_{\rm ESO}$ and the number of ESO layers, $V_{\rm B}$ shows a decreasing trend. $V_{\rm B}$ of the Conv. and the Prop. are 727 V and 671 V, respectively.

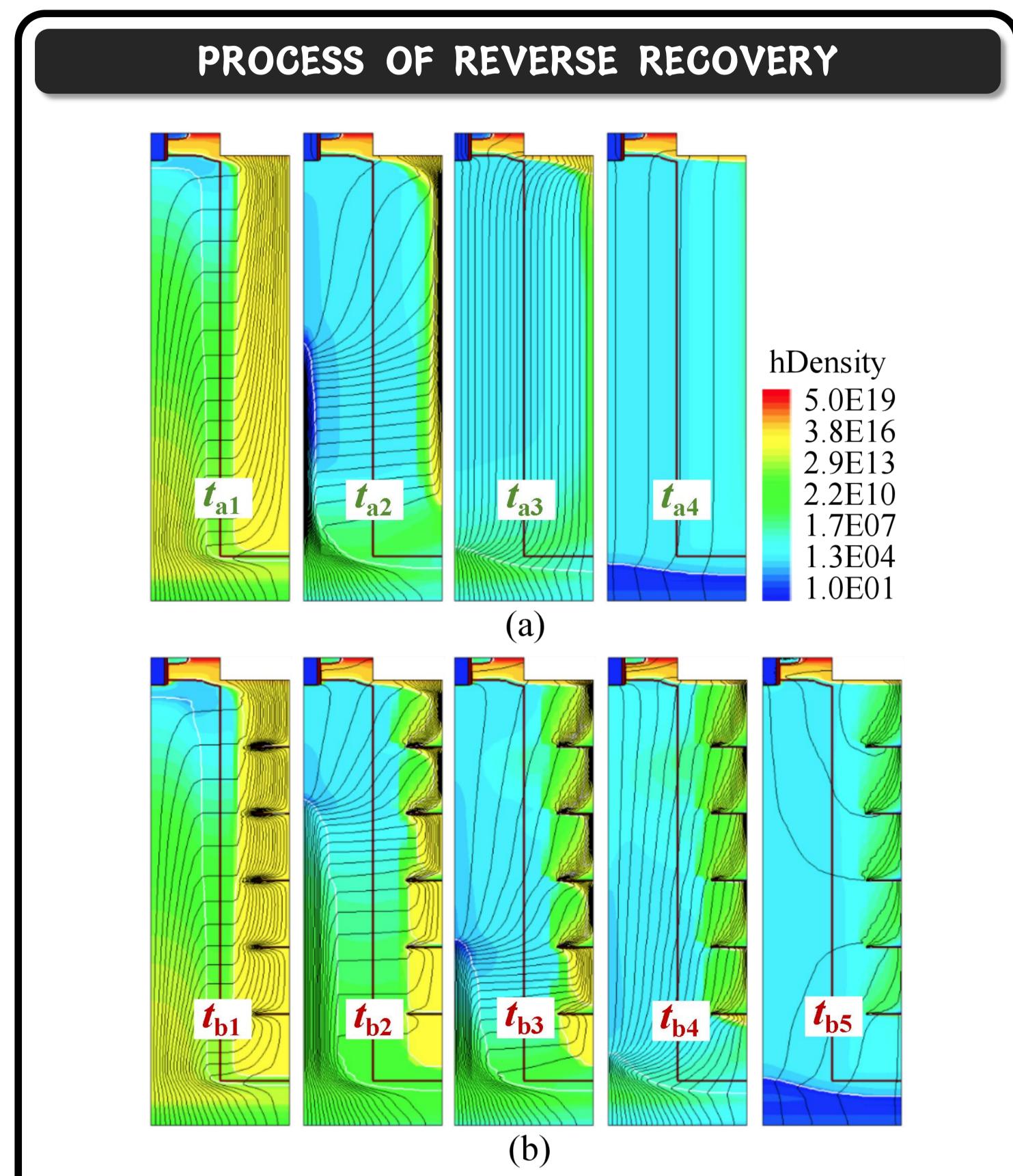


Figure 5. Distributions of hole density and current flowlines during t_2 - t_3 .

During the second stage $(t_{\rm b2}-t_{\rm b5})$, the stored holes will be extracted through the high-resistance depletion region. So, the second stage has a positive impact on decreasing d_{ir}/dt .

CONCLUSION

An SJ-MOSFET with a trench Schottky contact and an embedded SiO_2 insulator is proposed and investigated by simulations. At the reverse conduction state, the hole injection efficiency of the body diode can be lowered by adopting the trench contact, which helps to greatly reduce Q_{rr} . Besides, the SiO_2 insulator is embedded in the p-pillar, so the resistance of the whole p-pillar can be much higher than a uniformly doped p-pillar during reverse recovery, which contributes to suppressing reverse recovery oscillations

of the body diode. Simulation results indicate that, in comparison to the conventional SJ-MOSFET, the proposed SJ-MOSFET exhibits a 44% reduction in Q_{rr} and an 82% decrease in d_{r}/dt . Additionally, reverse recovery oscillations can be significantly suppresseed.

