

An Optimized Driver Design Strategy for Energy Storage System Applications

Qibin Wu¹, Ziqing Zheng¹, Dong Jie¹

Infineon Technologies Center of Competence (Shanghai) Co. Ltd., Shanghai, China

Corresponding Author: Qibin Wu, Corbyn.Wu@infineon.cn

1. Introduction

Energy storage systems (ESS) play a critical role in modern power grids by managing peak loads, smoothing fluctuations, and providing voltage and frequency support. However, short-term overload currents (1.5–3X rated capacity) impose significant challenges, including increased voltage stress, thermal demands, and accelerated device aging. Traditional fixed-parameter drivers prioritize extreme conditions, leading to significant efficiency losses during normal conditions.

This paper proposes a **dynamic driver optimization strategy** to adjust gate resistances (R_{gon}/R_{goff}) in real-time, achieving low switching losses during normal operations and improved reliability under overload conditions.

2. Challenges and Solutions

Challenges

- Voltage spikes caused by high di/dt and parasitic inductance during overload scenarios result in thermal stress and reliability risks.
- Fixed-parameter drivers are unable to balance efficiency and reliability across varying load conditions.

Solutions

- A dynamic adjustment strategy for gate resistances (R_{gon}/R_{goff}) based on real-time load classifications ensures:
- Reduced switching losses in normal operations.
- Enhanced reliability under overload conditions.

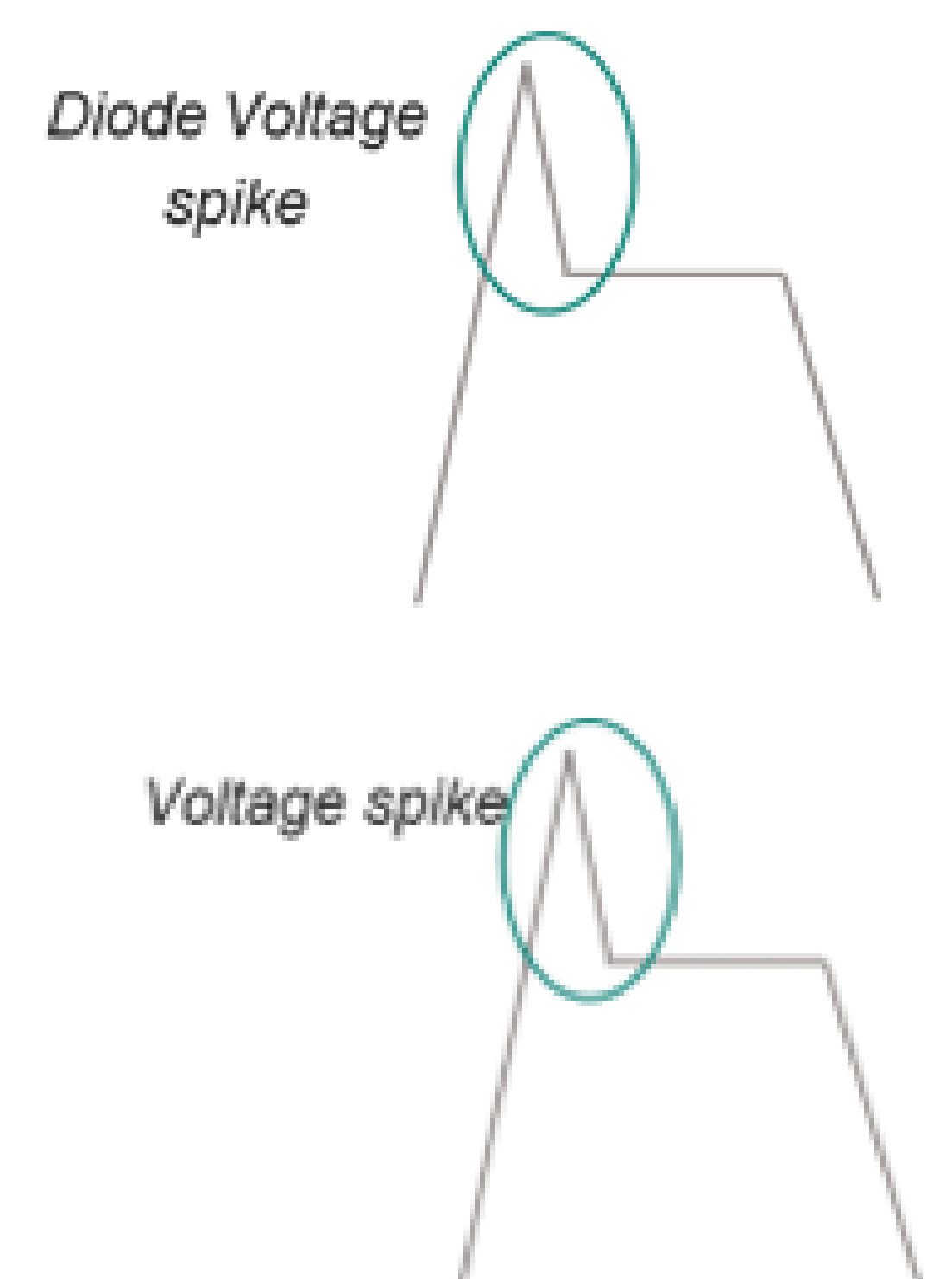
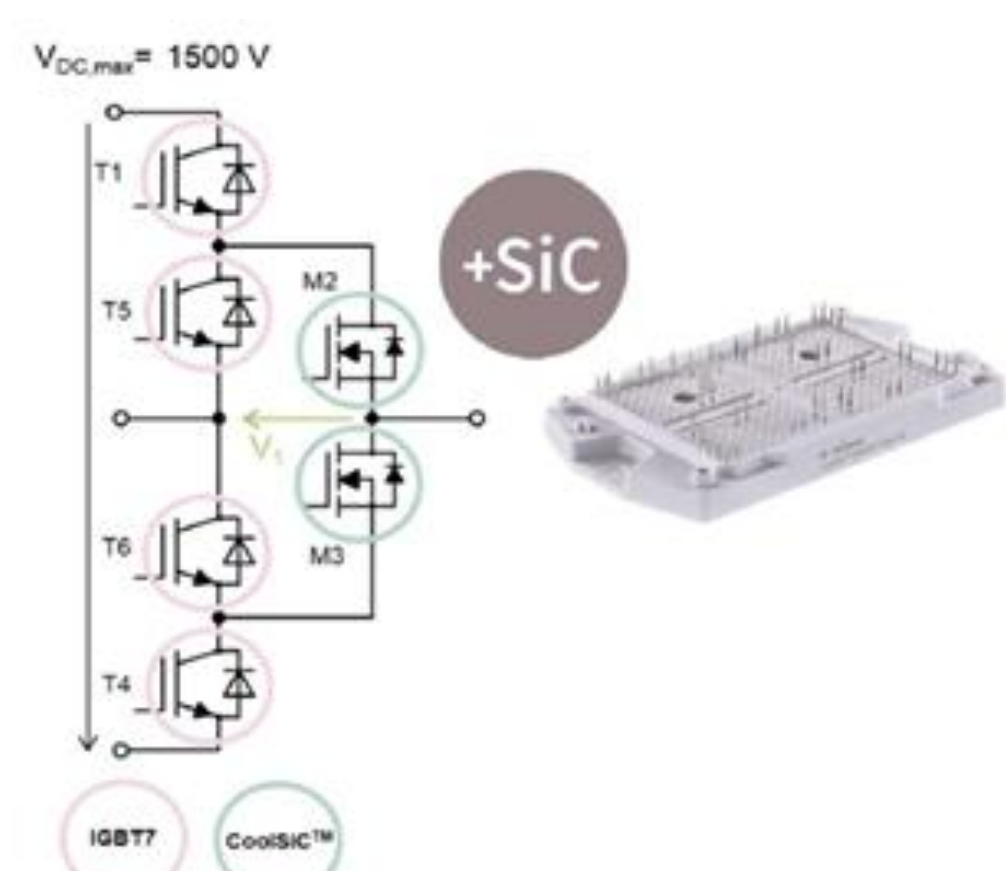


Fig. 1: Turn-on and turn-off voltage spike diagrams for power devices

3. Proposed Sectional Driver Strategy

The proposed sectional driver strategy dynamically switches between two operating modes:



- **Normal Mode:** Small R_{gon}/R_{goff} values are used to minimize switching losses and improve efficiency.
- **Overload Mode:** Large R_{gon}/R_{goff} values suppress di/dt , reduce voltage spikes, and enhance reliability.

This strategy is implemented using **Infineon's 2L-SRC gate driver chip**, which enables real-time parameter adjustment to optimize performance under diverse load conditions.

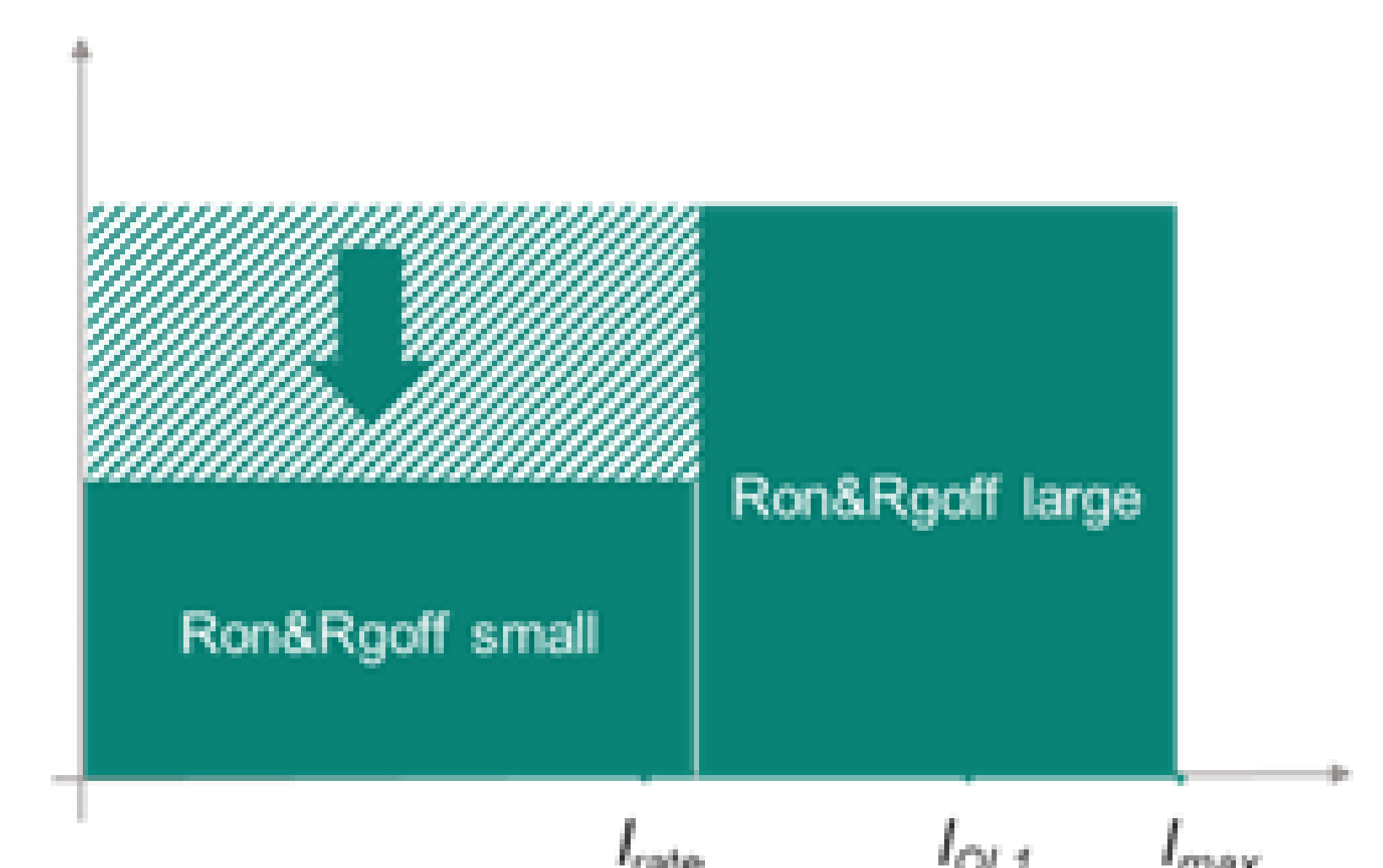


Fig 2: Flowchart of the sectional driver strategy and SiC module topology

4. Experimental Results

4.1 Turn-Off Resistance (R_{goff})

Key Findings:

- Increasing R_{goff} suppresses voltage spikes during the turn-off process.
- Higher R_{goff} values lead to increased turn-off losses, requiring a balance between reliability and efficiency.



Fig. 3. Relationship between the turn-off resistor parameters and turn-off voltage spikes

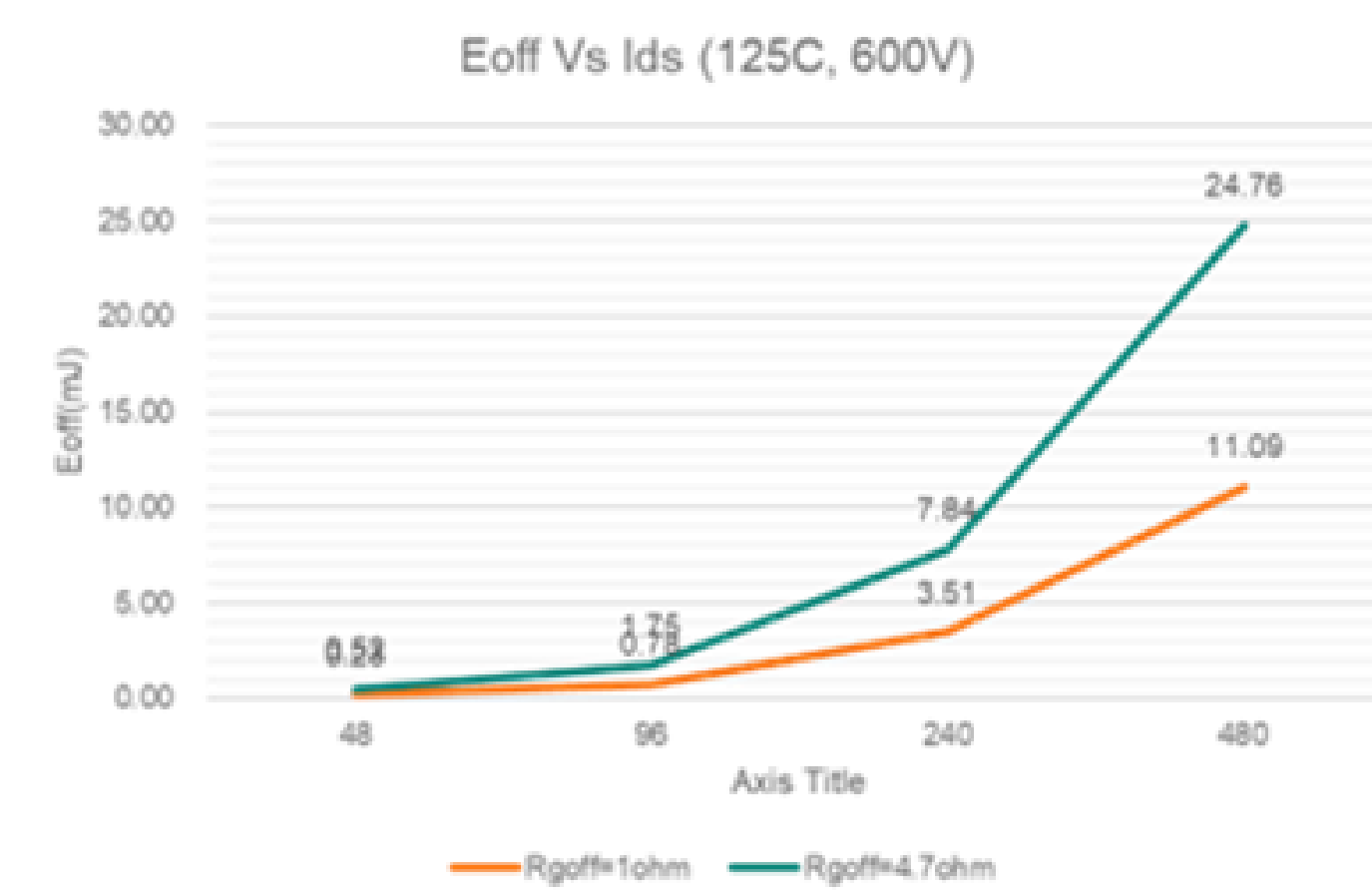


Fig. 4. Effect of turn-off resistance (R_{goff}) on turn-off loss (E_{off})

4.2 Turn-On Resistance (R_{gon})

Key Findings:

- Increasing R_{gon} reduces voltage spikes caused by reverse recovery during turn-on, which is critical in high-current scenarios.
- Larger R_{gon} values increase turn-on losses, making dynamic adjustment essential for balancing performance.

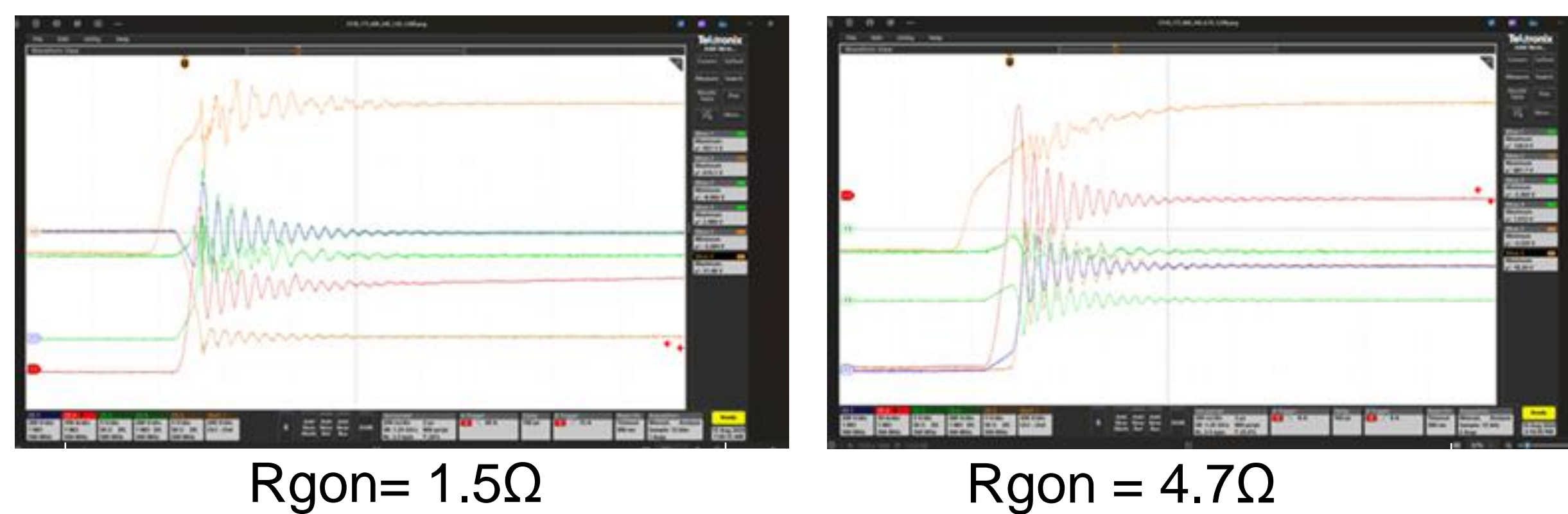


Fig. 5. Relationship between the turn-on resistor parameters and peak voltage spikes

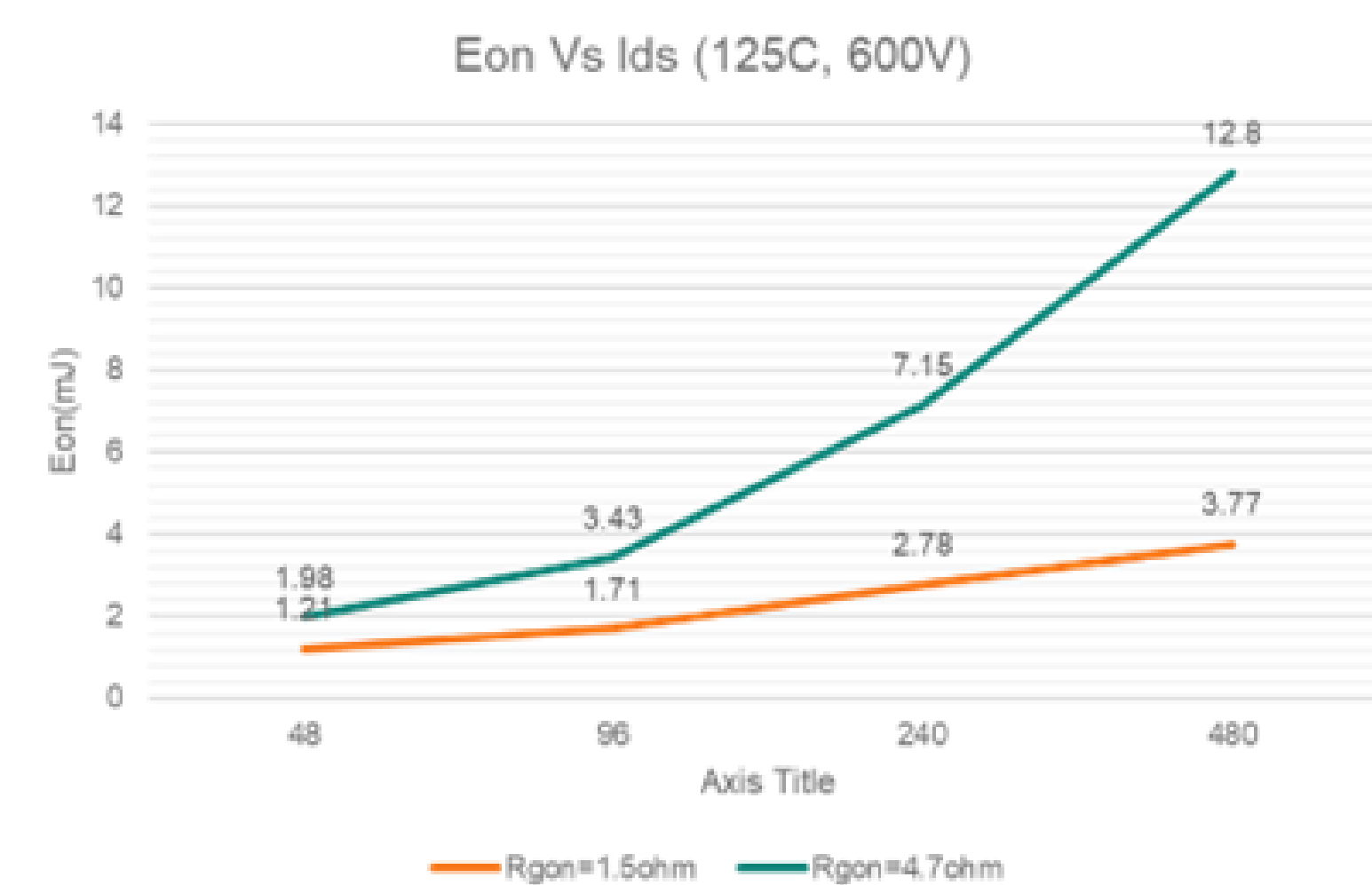


Fig. 6. Effect of the R_{gon} on E_{on}

4.3 Thermal and Efficiency Improvements

Thermal Performance:

- Dynamic adjustment reduces junction temperature by 12.7°C (from 129.2°C to 112.8°C).

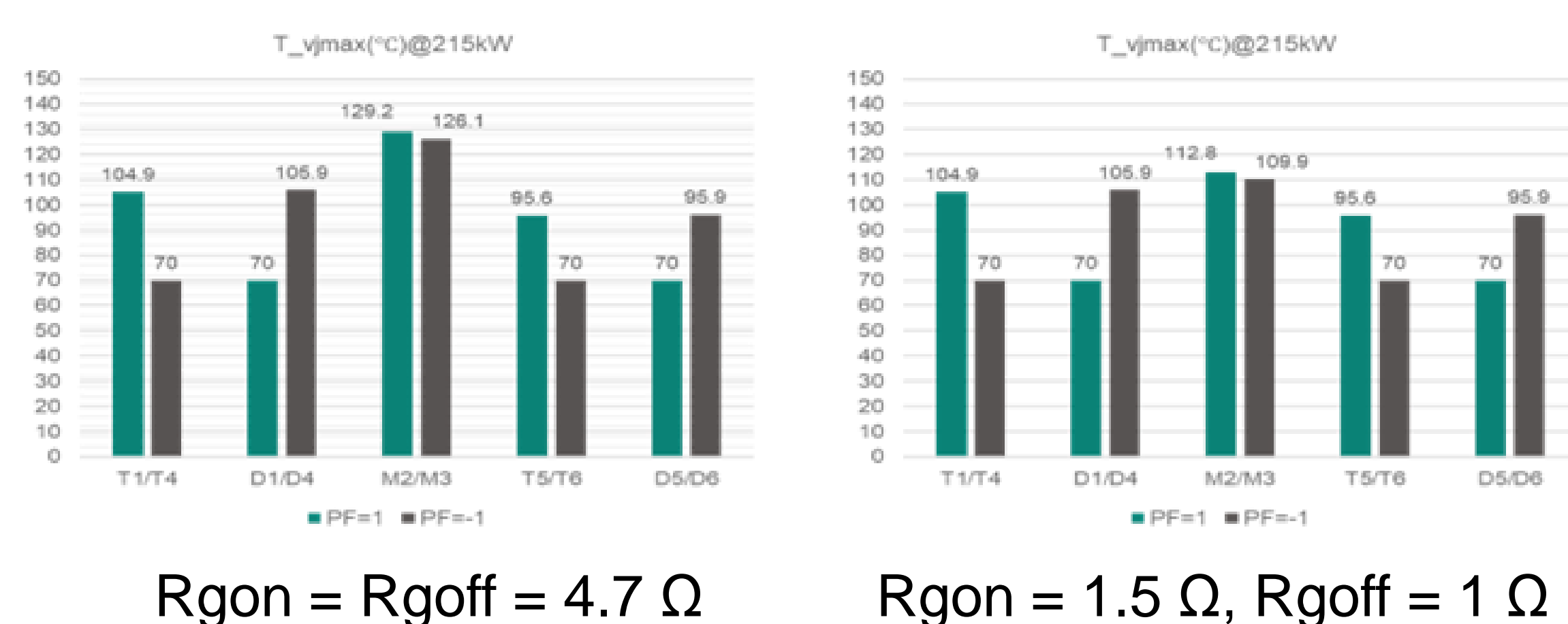


Fig. 7. Thermal performance of the module for a 215 kW ESS

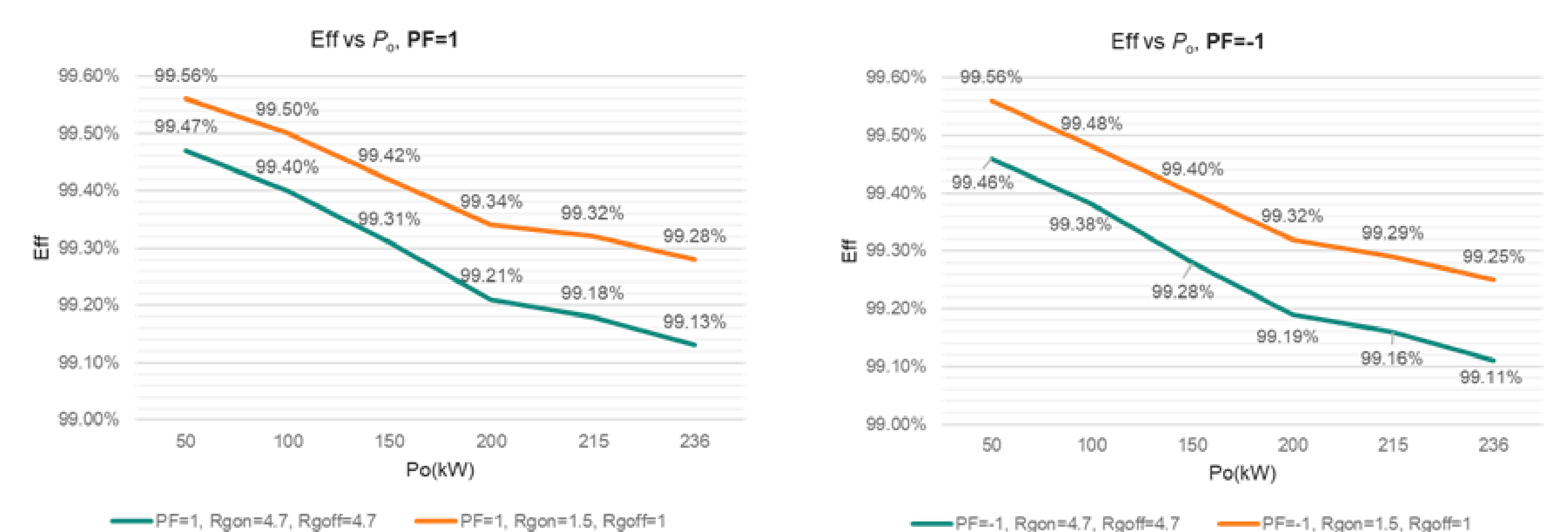


Fig. 8. Efficiency comparison under charging and discharging conditions

Efficiency Gains:

- Approximately 0.15% improvement in efficiency during charging and discharging modes.

5. Conclusion

The proposed **sectional driver strategy** dynamically adjusts R_{gon}/R_{goff} based on load conditions, achieving:

- Optimal efficiency during normal operations.
- Enhanced reliability under overload conditions.

Experimental validation on a 215 kW SiC-based ESS demonstrated significant improvements in system stability, thermal management, and energy efficiency.

This approach is scalable and **suitable for grid-forming** renewable energy systems, such as wind and photovoltaic (PV) systems.